

	Typ	L #	Hits	Search Text	DBs	Time Stamp
23	BRS	L23	142	(analog with simulat\$3) and (simulation with delay)	USPAT; EPO; JPO; DERWEN T	2003/12/04 14:06
24	BRS	L24	72	23 and puls\$3	USPAT; EPO; JPO; DERWEN T	2003/12/04 14:06
25	BRS	L25	140	22 or 24	USPAT; EPO; JPO; DERWEN T	2003/12/04 14:06
26	BRS	L26	3980	circuit near1 simulat\$3	USPAT	2003/12/04 14:21
27	BRS	L27	428	26 and (simulation with speed)	USPAT	2003/12/04 14:25
28	BRS	L28	140	27 and puls\$3	USPAT	2003/12/04 14:22
29	BRS	L29	9	26 and (simulation with speed with puls\$3)	USPAT	2003/12/04 14:27
30	BRS	L30	289	analog with circuit with simulation	USPAT	2003/12/04 14:28
31	BRS	L31	49	30 and (simulation with speed)	USPAT	2003/12/04 14:28
32	BRS	L32	38	30 and (simulation with delay\$1)	USPAT	2003/12/04 14:29
33	BRS	L33	75	31 or 32	USPAT	2003/12/04 14:29

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	IS&R	L1	97	(("6560757") or ("5394346")) or ("6141631") or ("6237126") or ("5598348") or ("5703798") or ("5459673") or ("5528509") or ("5845233") or ("4263726") or ("5535146") or ("4878388") or ("5652716") or ("6181754") or ("6006022") or ("4490808") or ("4792913") or ("5025402") or ("5363320") or ("6212490") or ("6212490") or ("6339751") or ("5555201") or ("5572437") or ("5544067") or ("5623418") or ("5801958") or ("5867399") or ("5883809") or ("5933356") or ("6053947") or ("6182258") or ("6230114") or ("5502661") or ("5841967") or ("5862361") or ("6052524") or ("6058492") or ("6195627") or ("6195629") or ("6202042") or ("6212491") or ("6223142") or ("6212491") or ("6223142") or ("6263302") or ("6298452") or ("6347388") or	USPAT	2003/12/04 12:49
2	BRS	L2	50	1 and delay	USPAT	2003/12/04 12:49
3	BRS	L3	56	1 and delay\$3	USPAT	2003/12/04 12:50
4	BRS	L4	8	3 and puls\$3 and resistance	USPAT	2003/12/04 12:52
5	BRS	L5	1062	analog with circuit with simulat\$3	USPAT; EPO; JPO; DERWEN T	2003/12/04 12:53
6	BRS	L6	11	5 and (reduce with delay\$1)	USPAT; EPO; JPO; DERWEN T	2003/12/04 12:54

	Type	L #	Hits	Search Text	DBs	Time Stamp
7	BRS	L7	25	5 and (reduc\$4 with delay\$1)	USPAT; EPO; JPO; DERWEN T	2003/12/04 13:26
8	BRS	L8	14	7 and pulse	USPAT; EPO; JPO; DERWEN T	2003/12/04 13:27
9	BRS	L9	62	5 and (pulse with delay)	USPAT; EPO; JPO; DERWEN T	2003/12/04 13:32
10	BRS	L10	35	pulse adj2 reduce\$1 adj2 delay	USPAT; EPO; JPO; DERWEN T	2003/12/04 13:35
11	BRS	L11	25606	pulse adj4 delay	USPAT; EPO; JPO; DERWEN T	2003/12/04 13:36
12	BRS	L12	12901	pulse adj2 delay	USPAT; EPO; JPO; DERWEN T	2003/12/04 13:36
13	BRS	L13	77	11 and (analog with simulat\$3)	USPAT; EPO; JPO; DERWEN T	2003/12/04 13:51
14	BRS	L14	433	(analog and circuit and simulator).ti,ab.	USPAT; EPO; JPO; DERWEN T	2003/12/04 13:52

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15	BRS	L15	36	14 and pulse and delay	USPAT; EPO; JPO; DERWEN T	2003/12/04 13:57
16	BRS	L16	8	14 and (increase with speed)	USPAT; EPO; JPO; DERWEN T	2003/12/04 14:00
17	BRS	L17	2	16 and pulse	USPAT; EPO; JPO; DERWEN T	2003/12/04 14:01
18	BRS	L18	3	(analog with simulator) and (pulse same (increase with speed))	USPAT; EPO; JPO; DERWEN T	2003/12/04 14:04
19	BRS	L19	1	(analog with simulator) and (pulse same (reduce with delay))	USPAT; EPO; JPO; DERWEN T	2003/12/04 14:03
20	BRS	L20	4	18 or 19	USPAT; EPO; JPO; DERWEN T	2003/12/04 14:03
21	BRS	L21	200	(analog with simulat\$3) and (simulation with speed)	USPAT; EPO; JPO; DERWEN T	2003/12/04 14:06
22	BRS	L22	86	21 and puls\$3	USPAT; EPO; JPO; DERWEN T	2003/12/04 14:05

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23	BRS	L23	142	(analog with simulat\$3) and (simulation with delay)	USPAT; EPO; JPO; DERWEN T	2003/12/04 14:06
24	BRS	L24	72	23 and puls\$3	USPAT; EPO; JPO; DERWEN T	2003/12/04 14:06
25	BRS	L25	140	22 or 24	USPAT; EPO; JPO; DERWEN T	2003/12/04 14:06